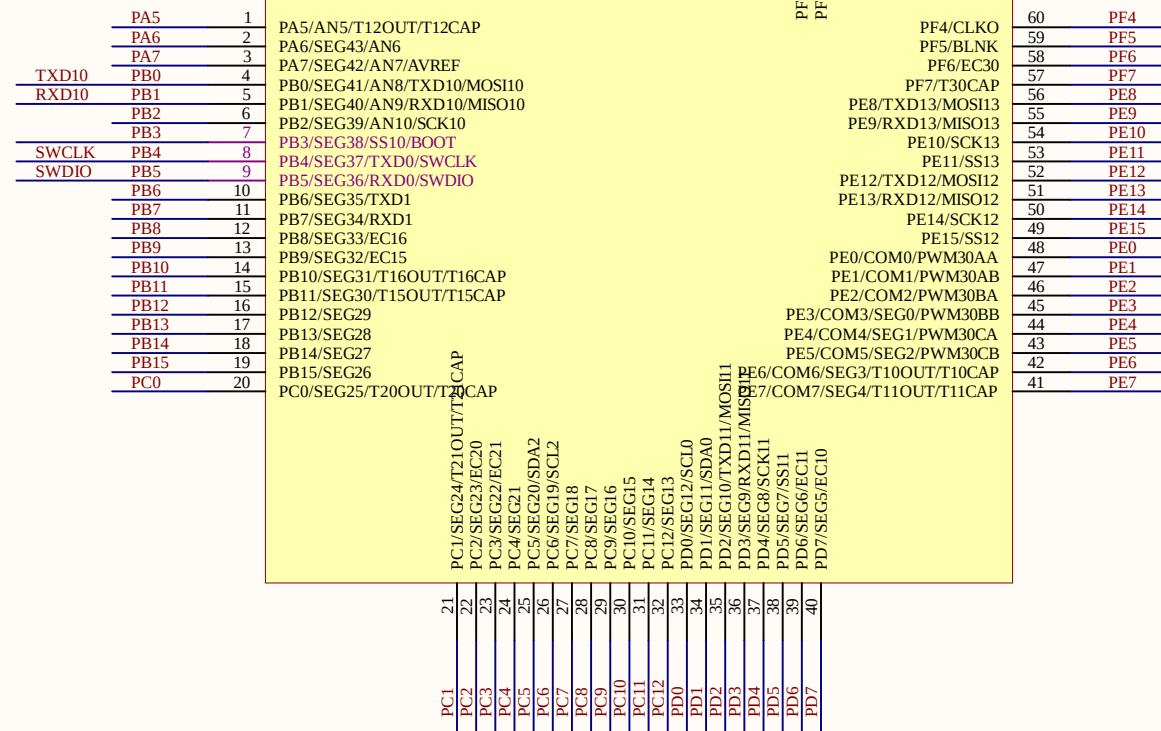




Boot Mode : Active "Low"



Timing diagram showing VDD (red) and PFI (green) signals. VDD transitions from 0V to 1V at 80 ns. PFI transitions from 0V to 1V at 69 ns. The diagram shows the relationship between VDD and PFI signals, with VDD rising before PFI.



U101
A31G123ML
80LQFP- 1212

